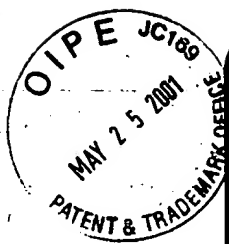


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PTO/SB/21 (6-98)
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TRANSMITTAL FORM (to be used for all correspondence after initial filing)	Application Number	09/834,660
	Filing Date	April 12, 2001
	First Named Inventor	Luan C. Tran
	Group Art Unit	2813
	Examiner Name	Unknown
Total Number of Pages in This Submission	3	Attorney Docket Number MI22-1637

ENCLOSURES (check all that apply)		
<input type="checkbox"/> Fee Transmittal Form	<input type="checkbox"/> Assignment Papers (for an Application)	<input type="checkbox"/> After Allowance Communication to Group
<input type="checkbox"/> Fee Attached	<input type="checkbox"/> Drawing(s)	<input type="checkbox"/> Appeal Communication to Board of Appeals and Interferences
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<input type="checkbox"/> Extension of Time Request	<input type="checkbox"/> Power of Attorney, Revocation Change of Correspondence Address	<input checked="" type="checkbox"/> Additional Enclosure(s) (please identify below):
<input type="checkbox"/> Express Abandonment Request	<input type="checkbox"/> Terminal Disclaimer	Postcard.
<input checked="" type="checkbox"/> Information Disclosure Statement	<input type="checkbox"/> Small Entity Statement	1449
<input type="checkbox"/> Certified Copy of Priority Document(s)	<input type="checkbox"/> Request for Refund	Prior art references.
<input type="checkbox"/> Response to Missing Parts/Incomplete Application	Remarks	No fees required.
<input type="checkbox"/> Response to Missing Parts under 37 CFR 1.52 or 1.53	The Commissioner is hereby authorized to charge any additional fees required under 37 CFR Sections 1.16 and 1.17 and credit any overpayments to: 23-0925.	

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT	
Firm or Individual name	Frederick M. Fliegel, Ph.D., Reg. No. 36,138 Wells, St. John, Roberts, Gregory & Matkin P.S. Customer No. 021567
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

O I P E
MAY 25 2001
PATENT & TRADEMARK

Application Serial No. 09/834,660
Filing Date April 12, 2001
Inventor Luan C. Tran
Assignee Micron Technology, Inc.
Group Art Unit 2813
Examiner Unknown
Attorney's Docket No. MI22-1637
Title: Semiconductor Processing Methods Of Forming Transistors, Semiconductor
Processing Methods Of Forming Dynamic Random Access Memory Circuitry, And
Related Integrated Circuitry

INFORMATION DISCLOSURE STATEMENT

References - - See attached Form PTO-1449

In compliance with 37 C.F.R. §§ 1.56, 1.97 and 1.98, your attention is directed to
the United States patents and other references listed on the attached Form PTO-1449.

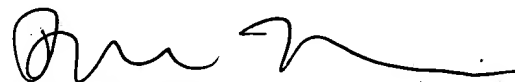
No admission is made regarding whether all the submitted references are prior art.

This Information Disclosure Statement is being filed within three months of the filing
date of the application or before the mailing of a first Office Action, whichever occurs last.
Therefore, no fee is believed to be required. However, in the event that a fee is required
for filing this Supplemental Information Disclosure Statement, please charge the fee
specified under 37 C.F.R. §1.17(p) to Deposit Account No. 23-0925. Please credit Deposit
Account No. 23-0925 with any overpayment of the above fee.

Citation of these references is respectfully requested.

Respectfully submitted,

Date: May 22, 2001



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